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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: O. SUZUKI, et al.

Serial No: 10/722,597

Filed: November 28, 2003

Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND  
SEMICONDUCTOR INTEGRATED CIRCUIT CHIP THEREOF

Group: 2822

Examiner: Monica LEWIS

RESPONSE TO NON-FINAL OFFICE ACTION

Mail Stop: Amendments  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 11, 2007

Sir:

In response to the outstanding further Non-Final Office Action, dated August 11, 2006, the period of response for which extension of time is requested in the attached Petition for Extension of Time, please enter the responsive Remarks as listed below and as set forth on the following pages:

**Remarks.**